

①

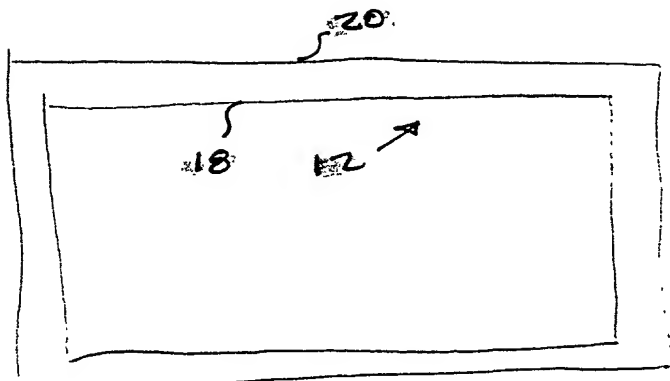


FIGURE 2  
(UPPER)

Layer 1  
shaped to  
perimeter of  
PCB

Layer 1  
tooling Pins

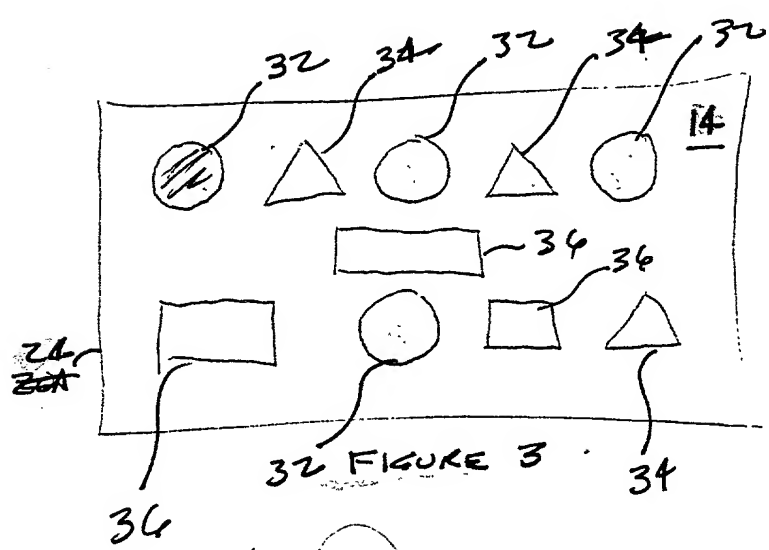


FIGURE 3

Layer 2

- ⊙ = smt cutout areas
- △ = PTH Component Cutout areas
- = PTH Lead Cutout areas
- white open

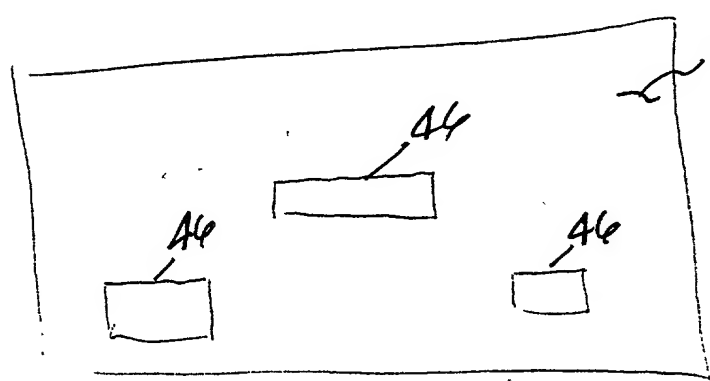


FIGURE 4

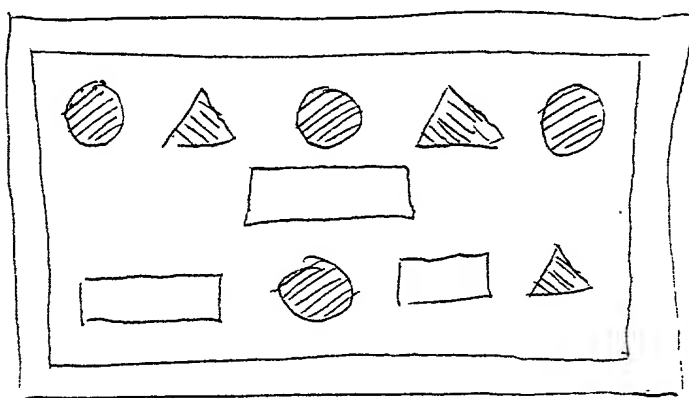
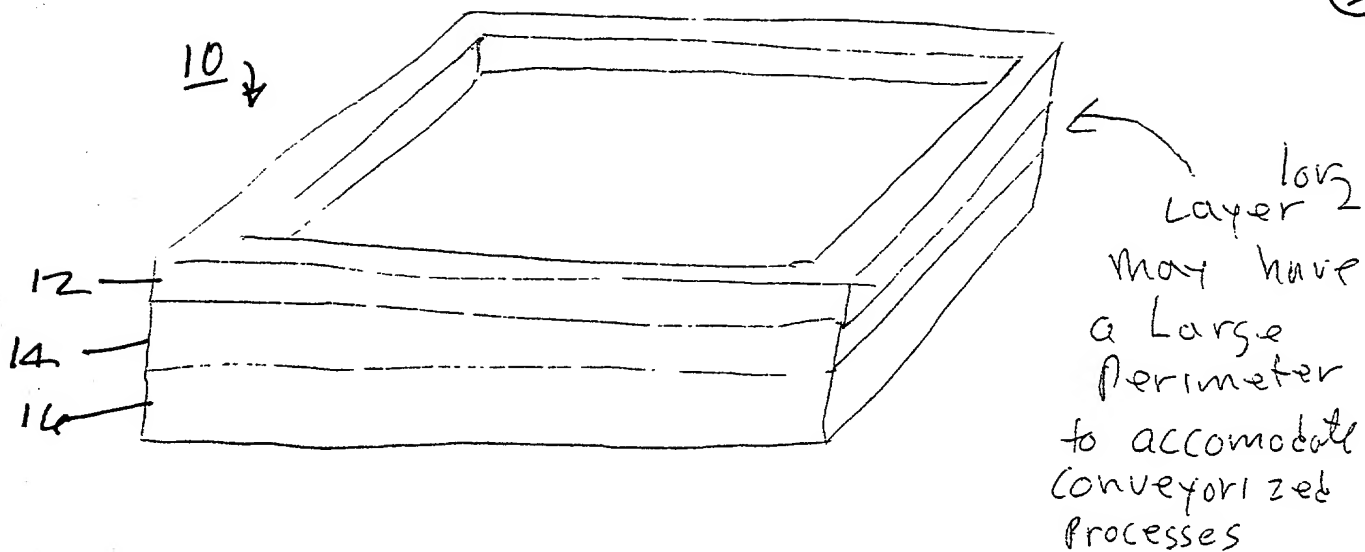
Layer 3

- = PTH Lead Cutout areas only

either  
or

200220 00018001

(2)



open only in Layer 2

= open thru Layer 2 + Layer 3.

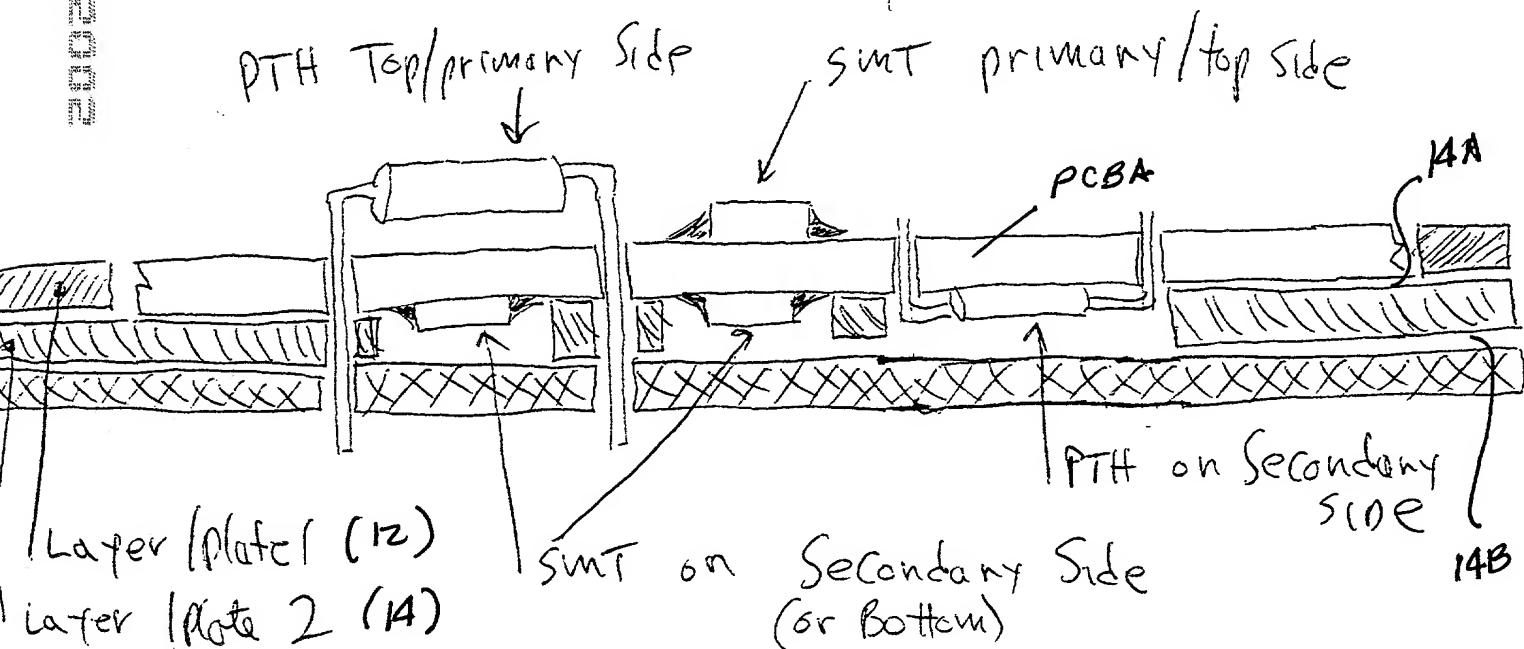


FIGURE 5

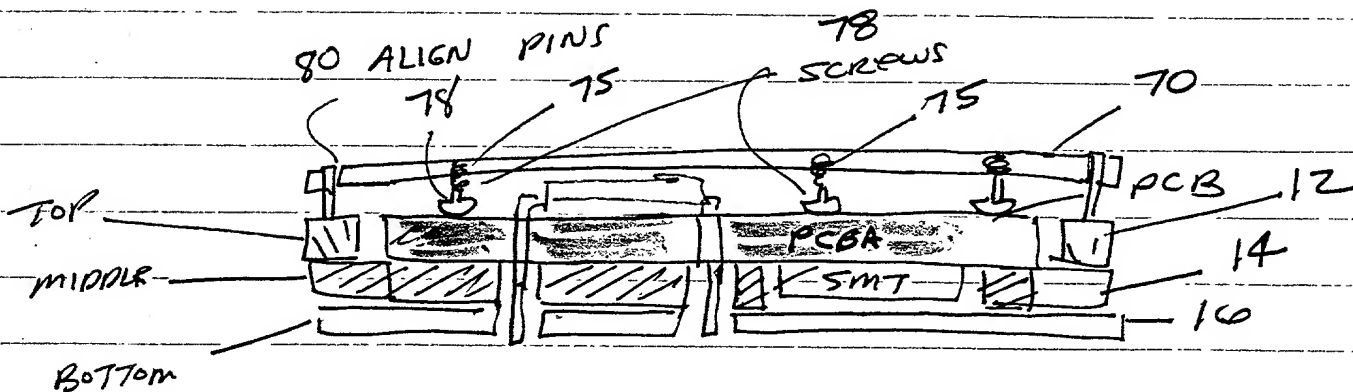


FIGURE 6

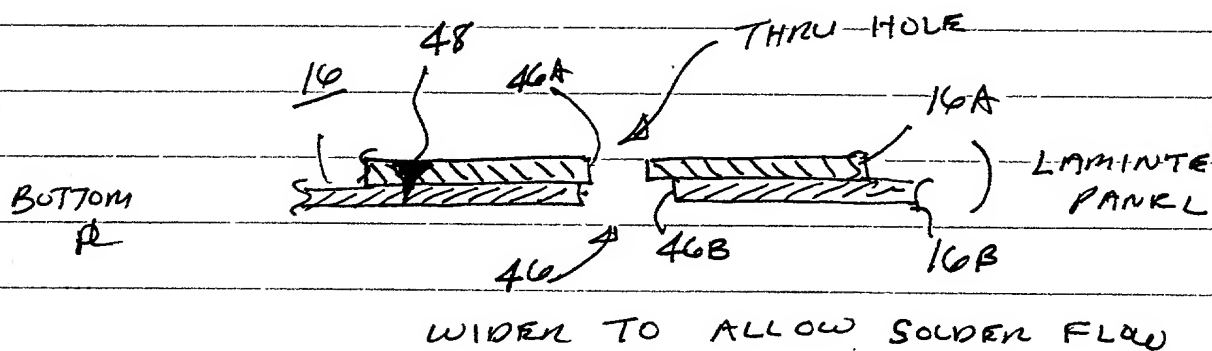


FIGURE 7

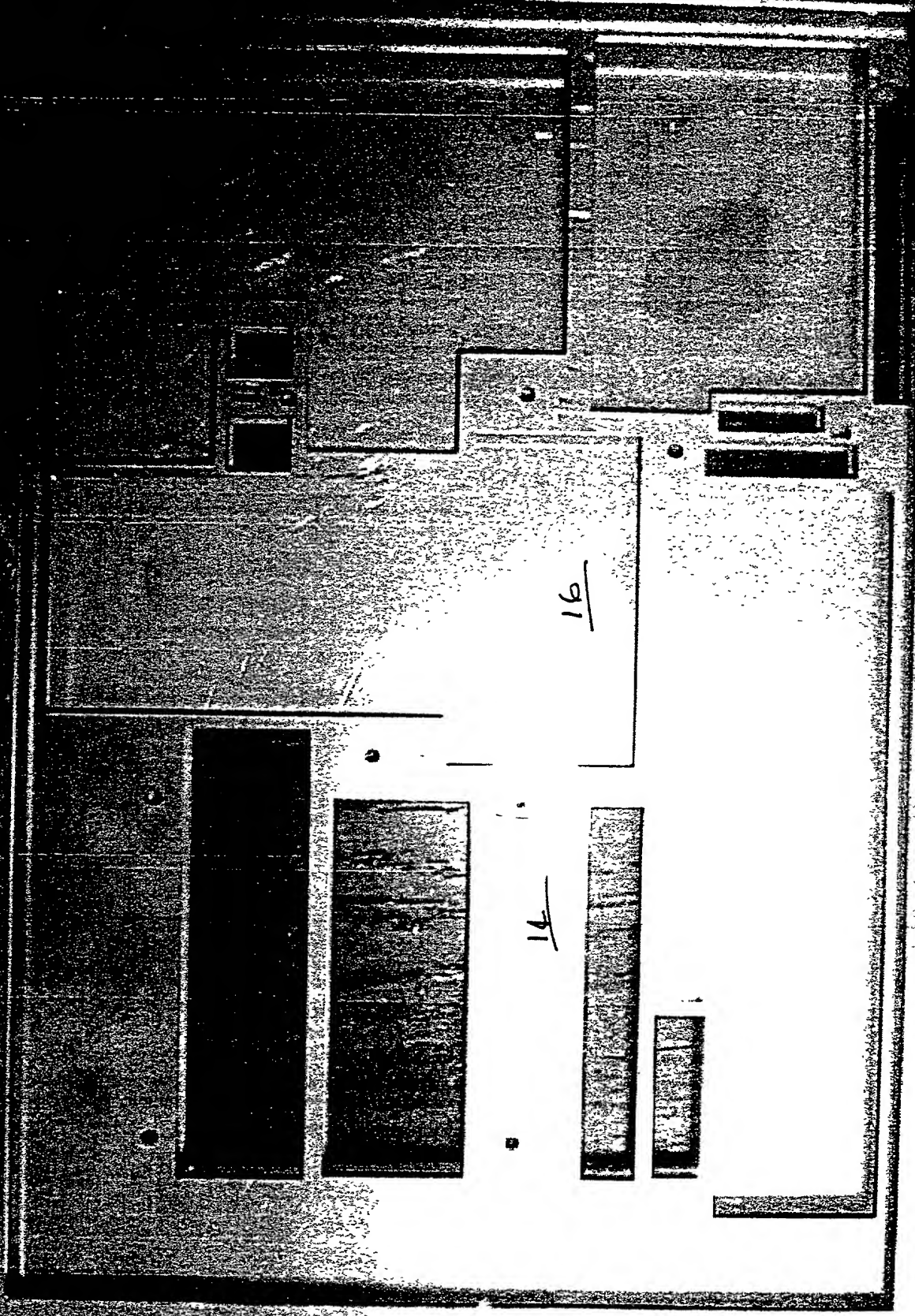


FIGURE 4